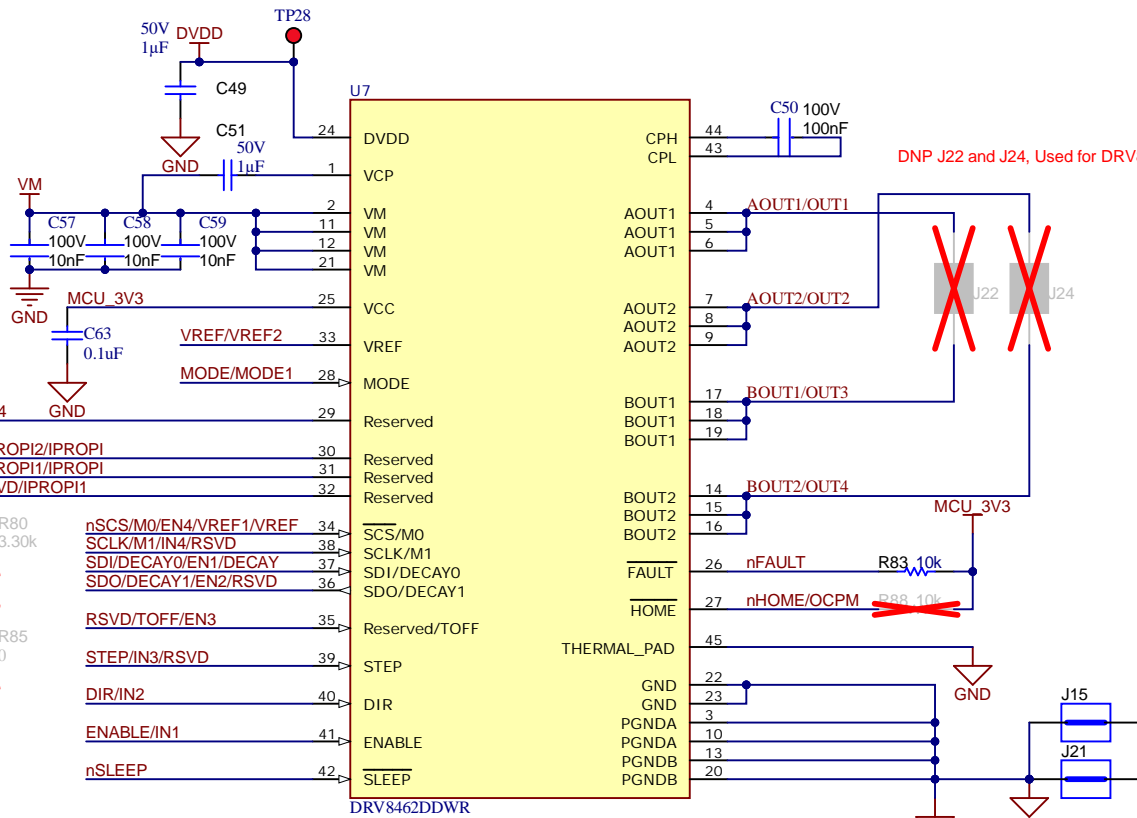
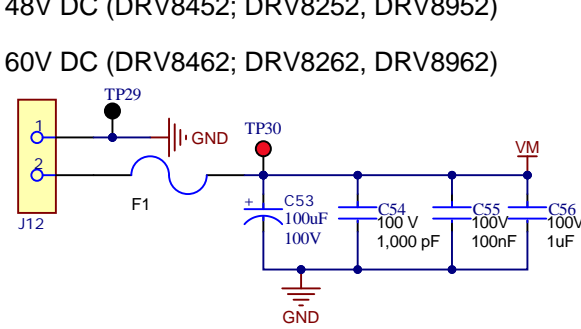


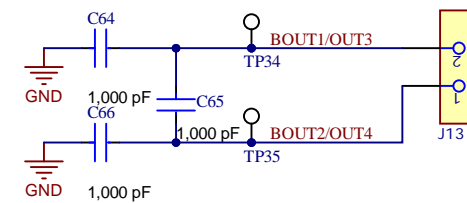
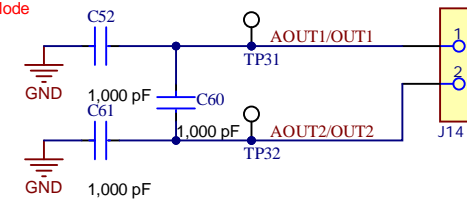
Motor Driver

4.5 - 48V DC (DRV8452; DRV8252, DRV8952)

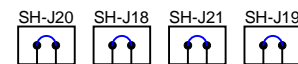
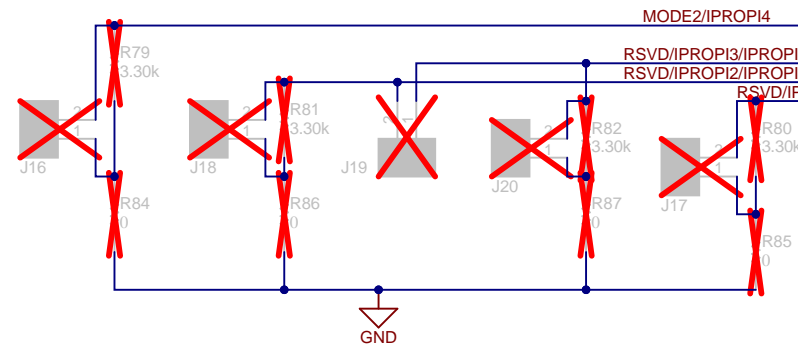
4.5 - 60V DC (DRV8462; DRV8262, DRV8962)



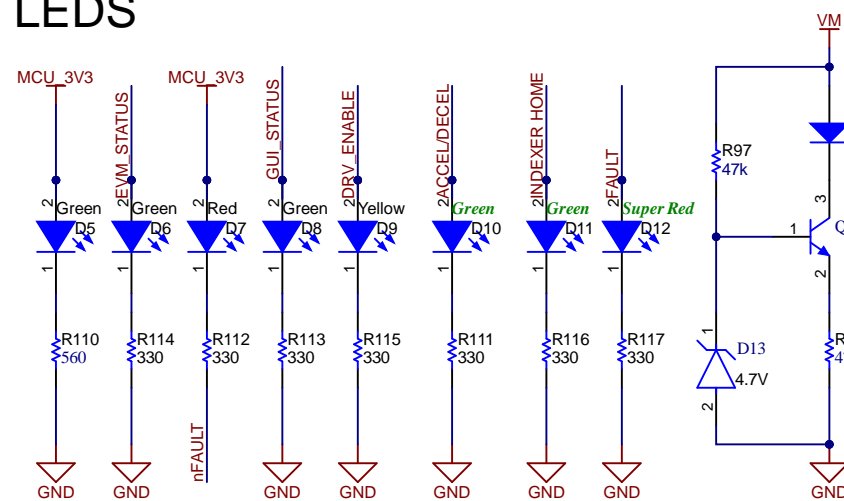
DNP J22 and J24, Used for DRV8262 Single H Bridge Mode



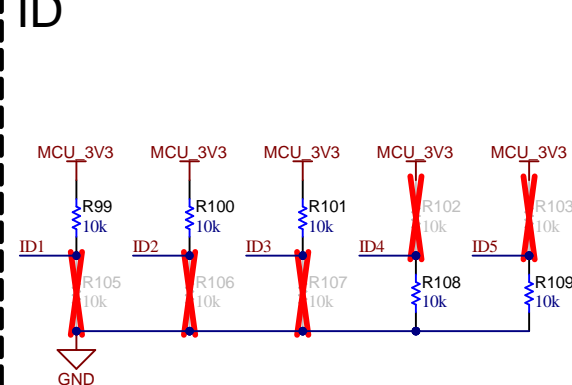
For DRV8962 and DRV8262 populate jumpers to disable current regulation



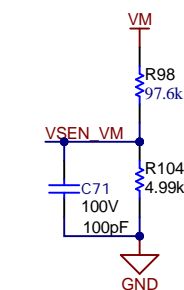
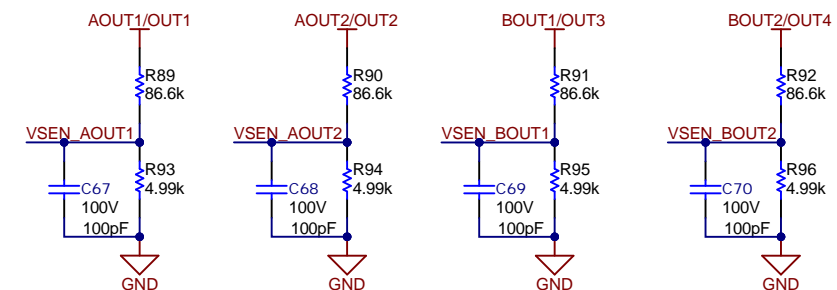
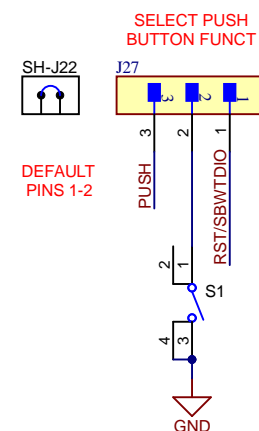
LEDS




ID

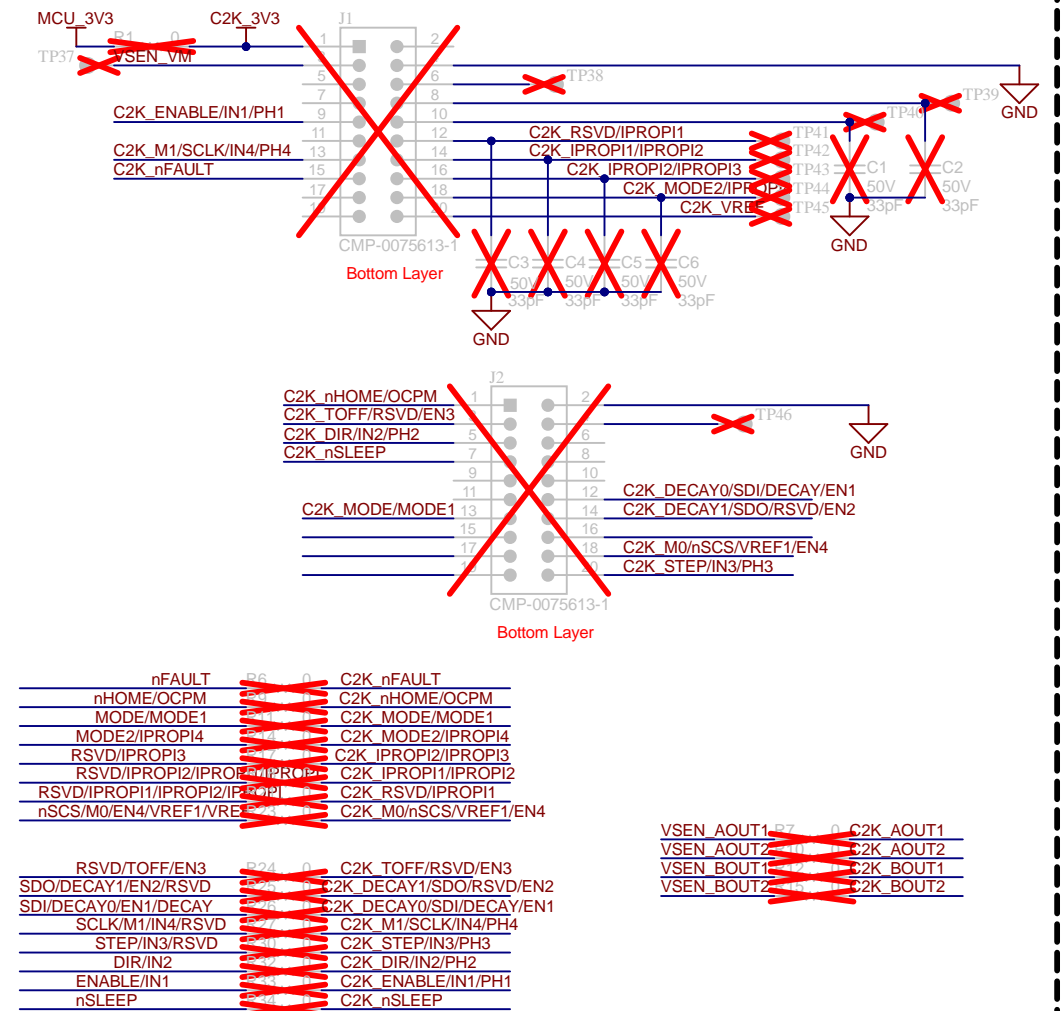
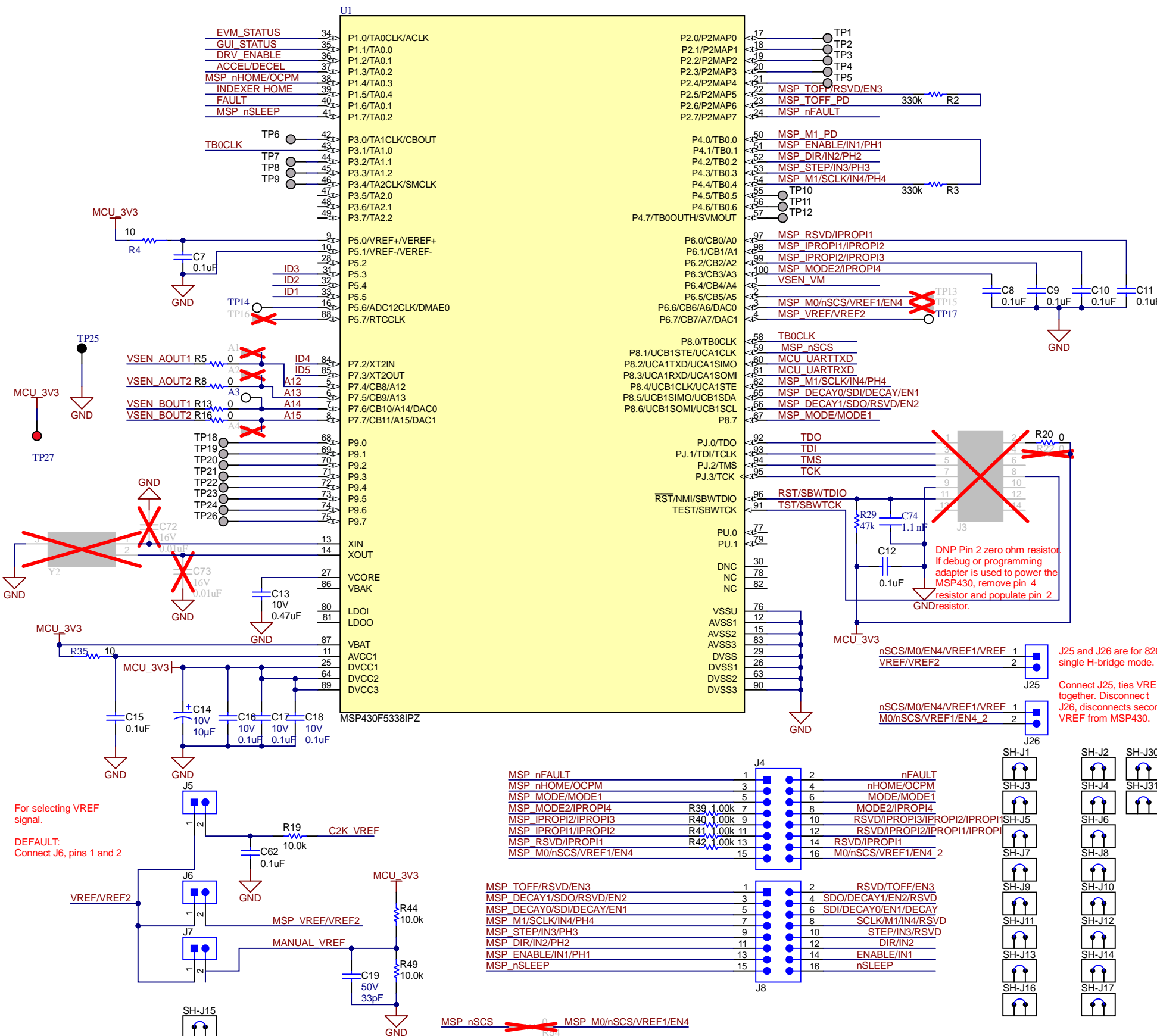
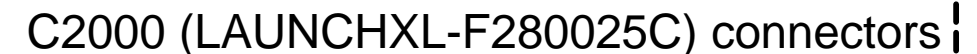


BUTTON



Orderable:	Designed for: Public Release	Mod. Date: 8/25/2022	
TID #: N/A	Project Title: DRV8452EVM		
Number: MD040	Rev: E2	Sheet Title:	
SVN Rev: 1d5cd23cf125c4a53b0405	025ef300072a685d012	Sheet: 2 of 3	
Drawn By:	File: MD040_Motor_Driver_RevE2_SchDoc	Size: B	
Engineer: Wang Li	Contact: http://www.ti.com/support	http://www.ti.com	© Texas Instruments 2021

MSP430F5338



nFAULT	R26	0	C2K nFAULT
nHOME/OCPM	R27	0	C2K nHOME/OCPM
MODE/MODE1	R28	0	C2K MODE/MODE1
MODE2/IPROP14	R29	0	C2K MODE2/IPROP14
RSVD/IPROP13	R30	0	C2K IPROP1/IPROP13
RSVD/IPROP12/IPROP11/IPROP10	R31	0	C2K IPROP1/IPROP12
RSVD/IPROP11/IPROP12/IPROP13	R32	0	C2K RSVD/IPROP11
nSCS/M0/EN4/VREF1/VREF2	R34	1	C2K M0/nSCS/VREF1/EN4
RSVD/TOFF/EN3	R34	0	C2K TOFF/RSVD/EN3
SDO/DECAY1/EN2/RSVD	R35	0	C2K DECAY1/SDO/RSVD/EN2
SDI/DECAY0/EN1/DECAY	R36	0	C2K DECAY0/SDI/DECAY/EN1
SCLK/M1/IN4/RSVD	R37	0	C2K M1/SCLK/IN4/PH4
STEP/IN3/RSVD	R38	0	C2K STEP/IN3/PH3
DIR/IN2	R39	0	C2K DIR/IN2/PH2
ENABLE/IN1	R40	0	C2K ENABLE/IN1/PH1
nSLEEP	R41	0	C2K nSLEEP

~~VSEN_AOUT1~~ ~~0~~ ~~C2K_AOUT1~~
~~VSEN_AOUT2~~ ~~0~~ ~~C2K_AOUT2~~
~~VSEN_BOUT1~~ ~~0~~ ~~C2K_BOUT1~~
~~VSEN_BOUT2~~ ~~0~~ ~~C2K_BOUT2~~

J25 and J26 are for 8262 single H-bridge mode.

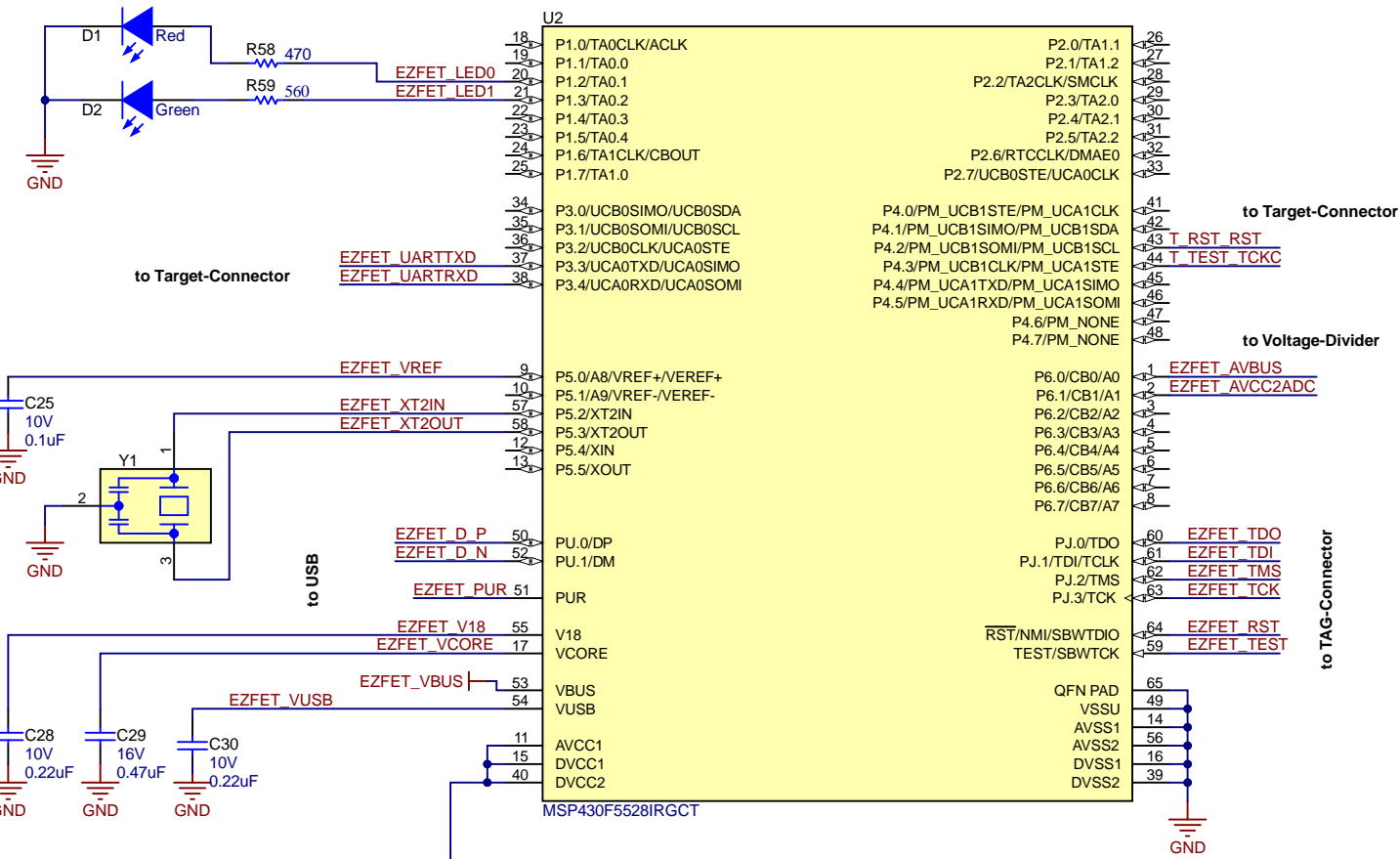
Connect J25, ties VREF s
together. Disconnect
J26, disconnects second
VREF from MSP430.

Signal	Value	Time (ns)
MSP nFAULT	1	0
MSP nHOME/OCPM	2	0
MSP nMODE/MODE1	3	0
MSP nMODE2/IPROPI4	4	0
MSP nPROPI2/IPROPI3	5	0
MSP nPROPI1/IPROPI2	6	0
MSP nRSDV/IPROPI1	7	0
MSP nM0/SCS/REF1/EN4	8	0
MSP nFAULT	2	50
MSP nHOME/OCPM	4	50
MSP nMODE/MODE1	6	50
MSP nMODE2/IPROPI4	8	50
MSP nPROPI2/IPROPI3	10	50
MSP nPROPI1/IPROPI2	12	50
MSP nRSDV/IPROPI1	14	50
MSP nM0/SCS/REF1/EN4	16	50
MSP nFAULT	1	100
MSP nHOME/OCPM	2	100
MSP nMODE/MODE1	3	100
MSP nMODE2/IPROPI4	4	100
MSP nPROPI2/IPROPI3	5	100
MSP nPROPI1/IPROPI2	6	100
MSP nRSDV/IPROPI1	7	100
MSP nM0/SCS/REF1/EN4	8	100

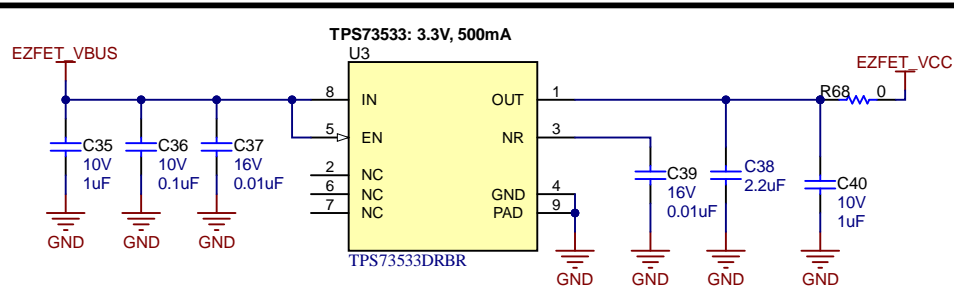
MSP_TOFF/RSVD/EN3	1	2	RSVD/TOFF/EN3
MSP_DECAY1/SDO/RSVD/EN2	3	4	SDO/DECAY1/EN2/RSVD
MSP_DECAY0/SDI/DECAY/EN1	5	6	SDI/DECAY0/EN1/DECAY
MSP_M1/SC/LIN4/PH4	7	8	SC/LM1/IN4/RSV
MSP_STEP/IN3/PH3	9	10	STEP/IN3/RSVD
MSP_DIR/IN2/PH2	11	12	DIR/IN2
MSP_ENABLE/IN1/PH1	13	14	ENABLE/IN1
MSP_nSLEEP	15	16	nSLEEP

MSP_nSCS MSP_M0/nSCS/VREF1/EN4

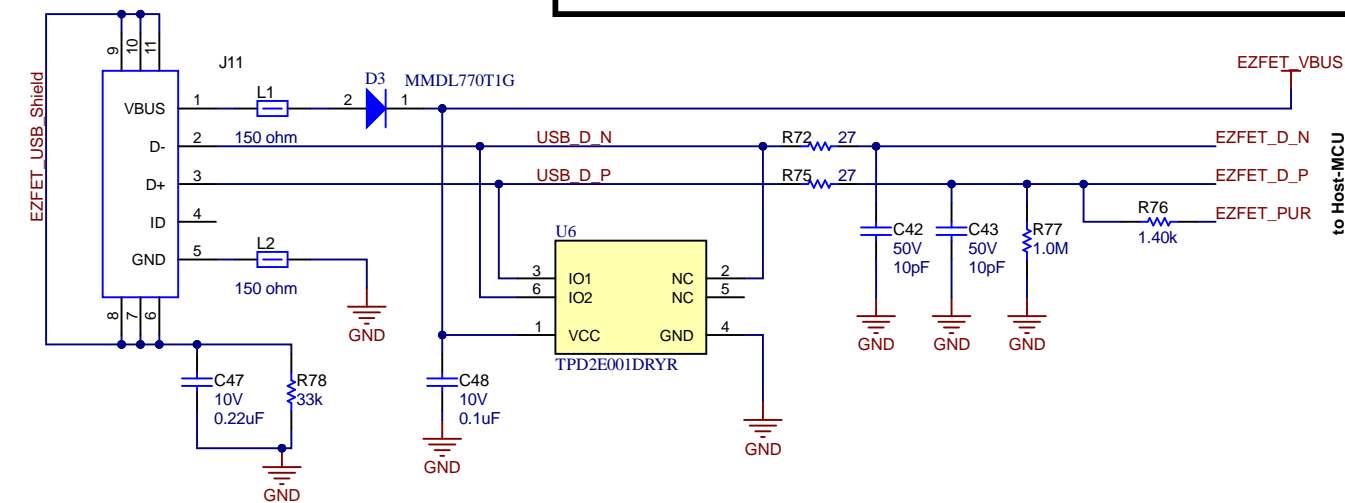
Host MCU for Emulation



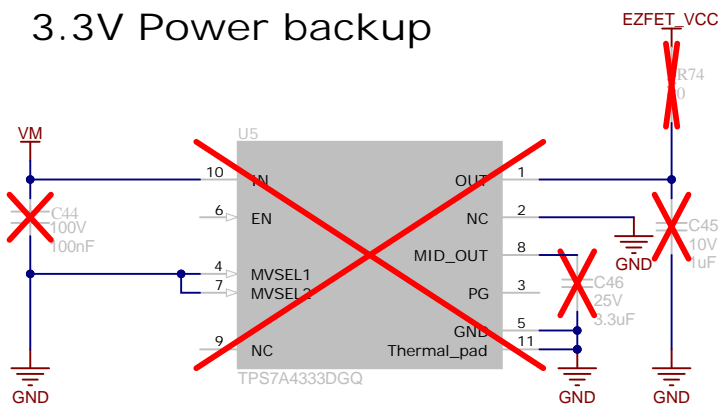
3.3V Power (EZFET_VCC)



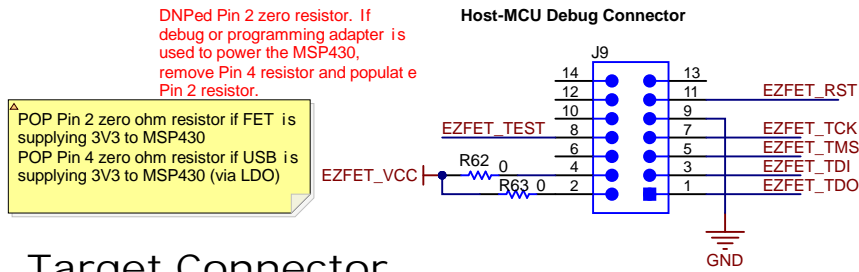
USB-I nterface



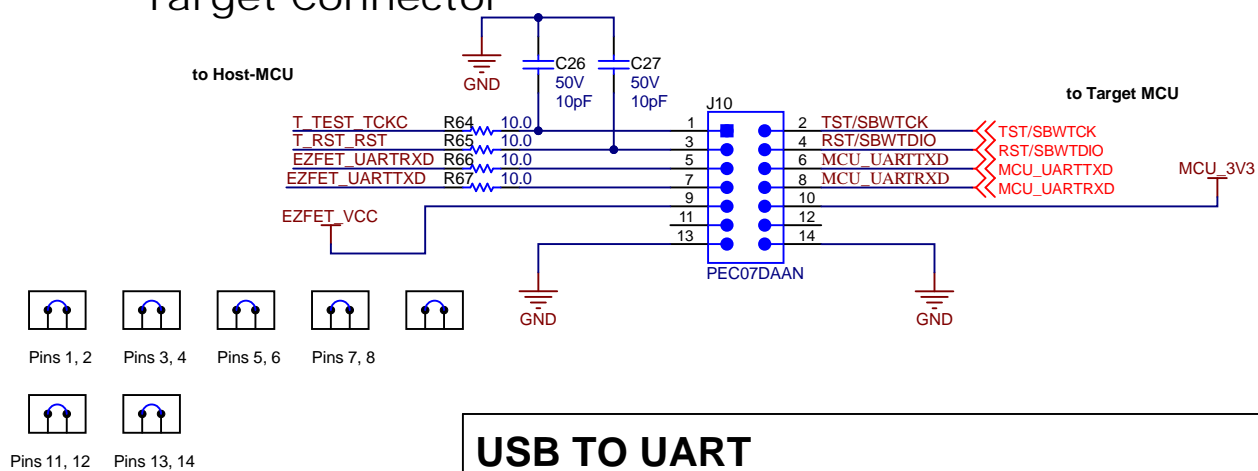
3.3V Power backup



JTAG-Connector (Host Debug)



Target Connector



USB TO UART

